OIPE JC177
OCT 0 5 2004
PATENT & TRADEMARK OFFICE

Replacement Sheet

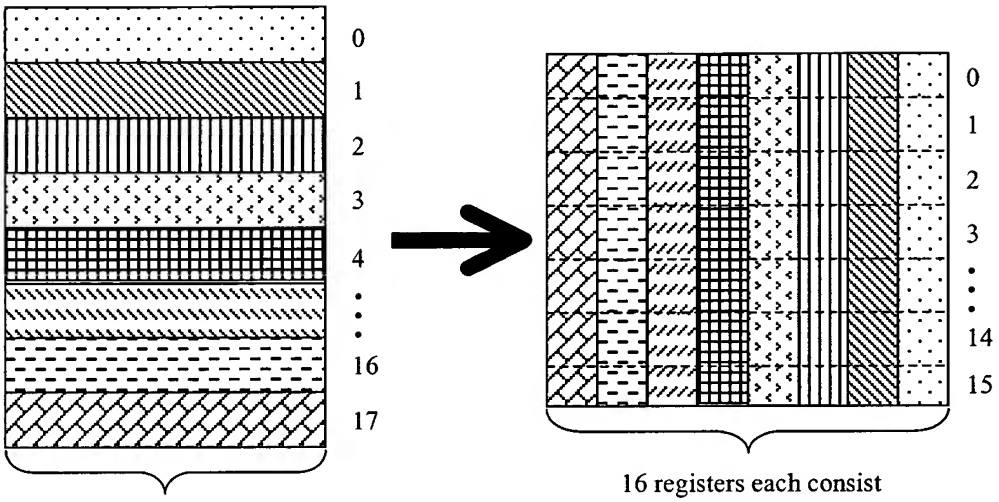
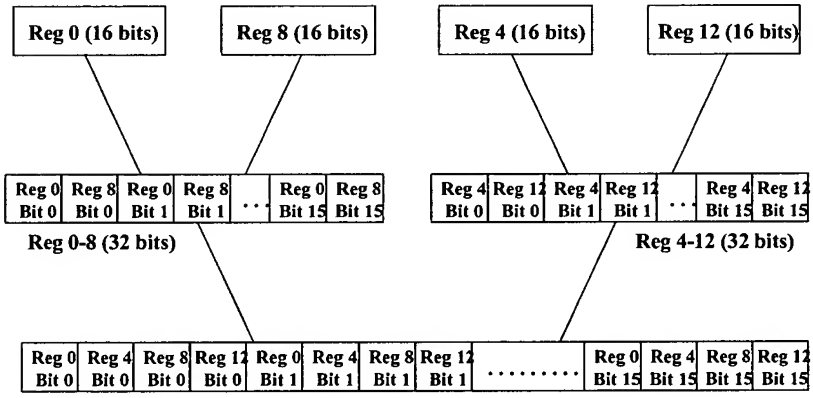


FIG. 5



Starting from Reg 0-8 then bit-interleaving with Reg 4-12 inserts bit 0 of Reg 4 between those of Reg 0 and 8, pushing bit 0 of Reg 8 to the right, and so on .... until the bits are ordered consecutively, from 0 to 15

FIG. 7

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Replacement Sheet

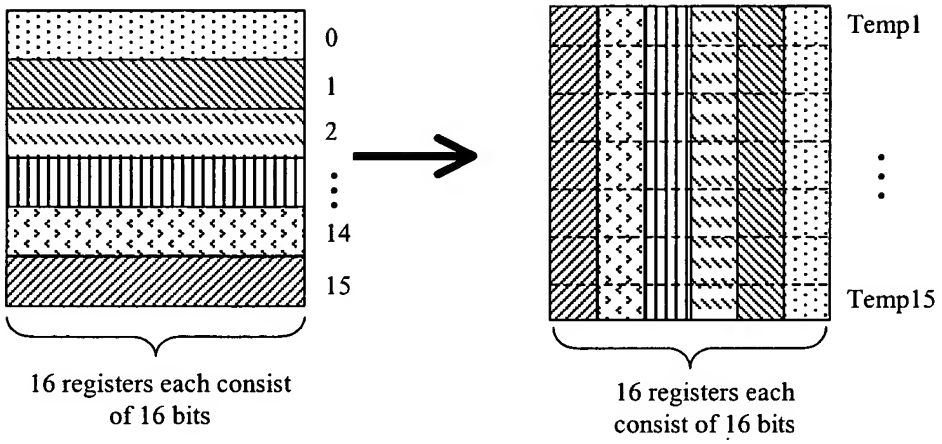

FIG. 8

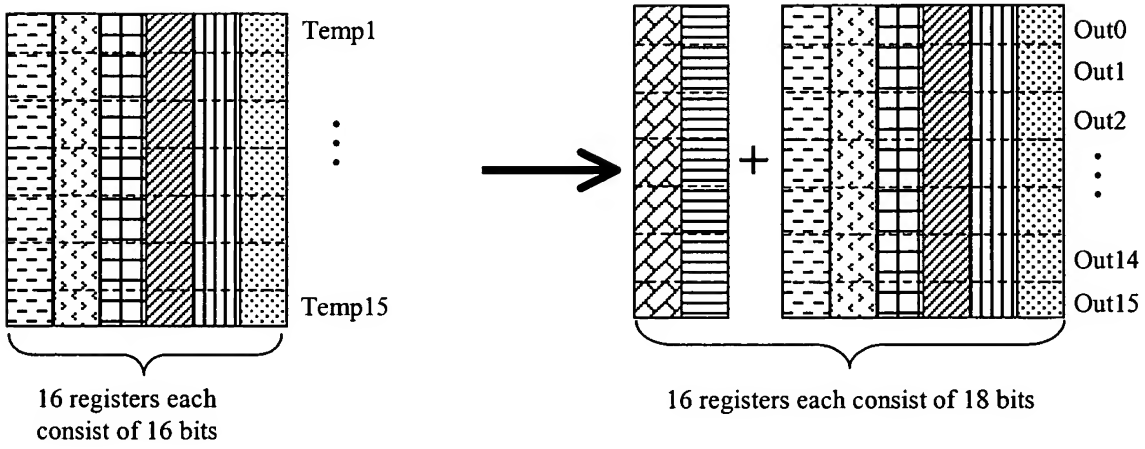

FIG. 9

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Replacement Sheet

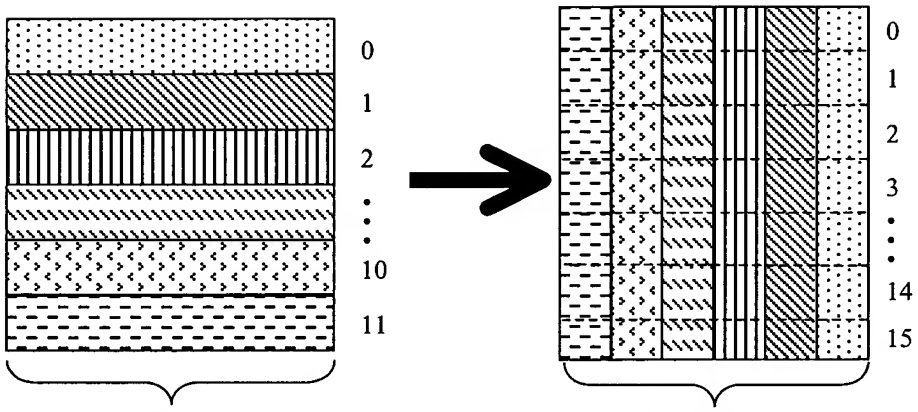

FIG. 10

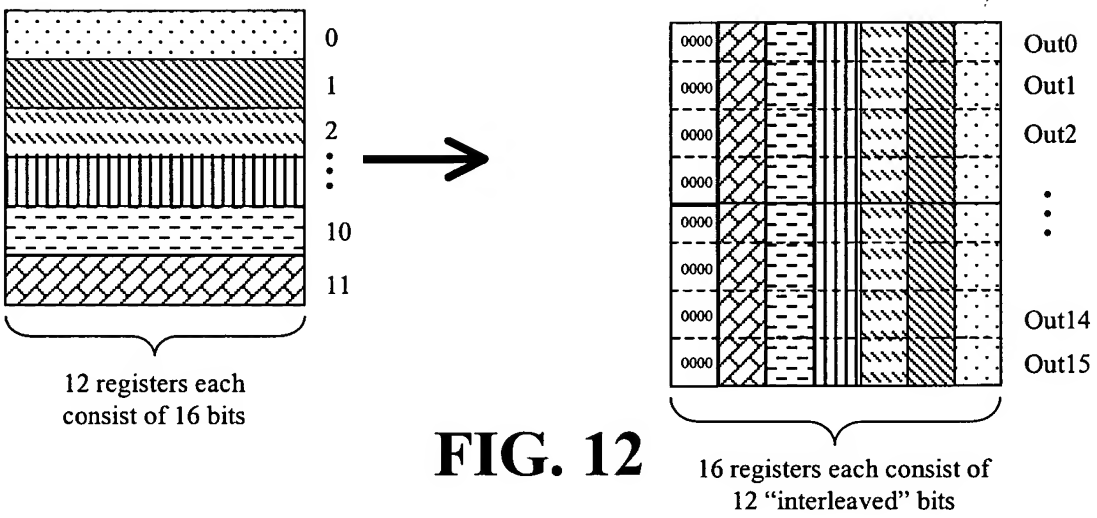

FIG. 12

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Replacement Sheet

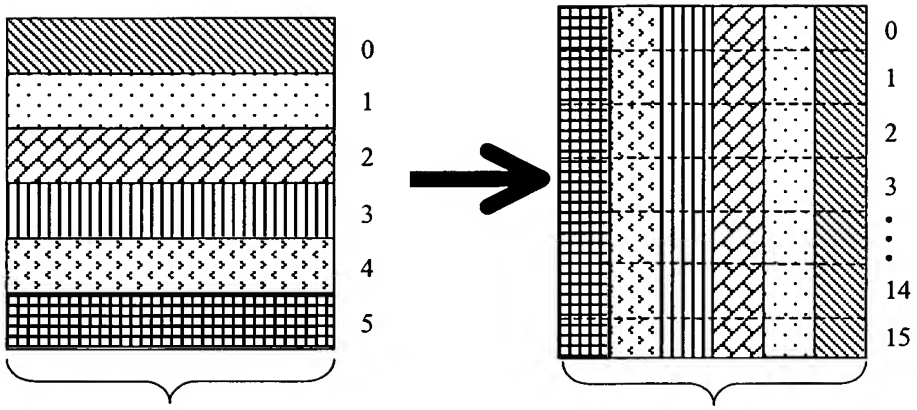

FIG. 13

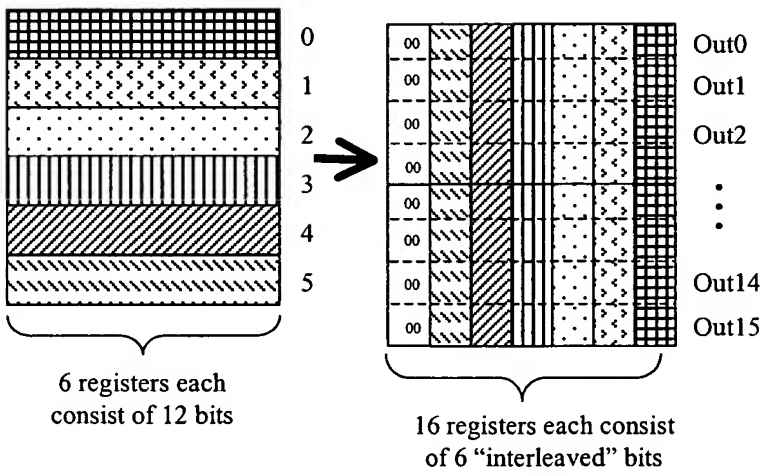

FIG. 15

Replacement Sheet

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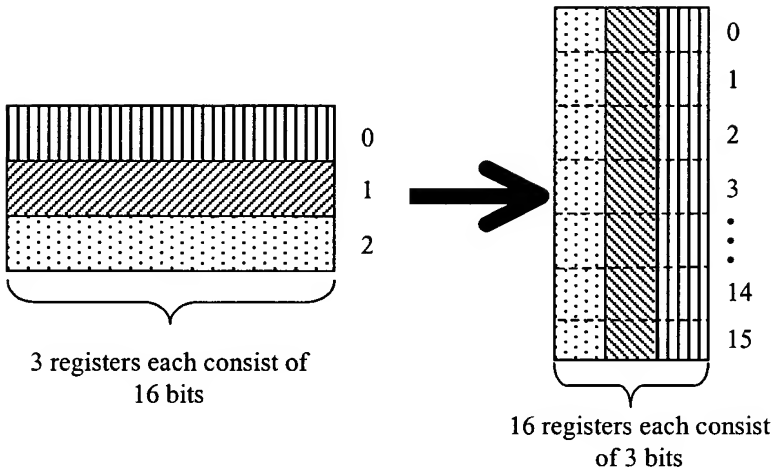

FIG. 16

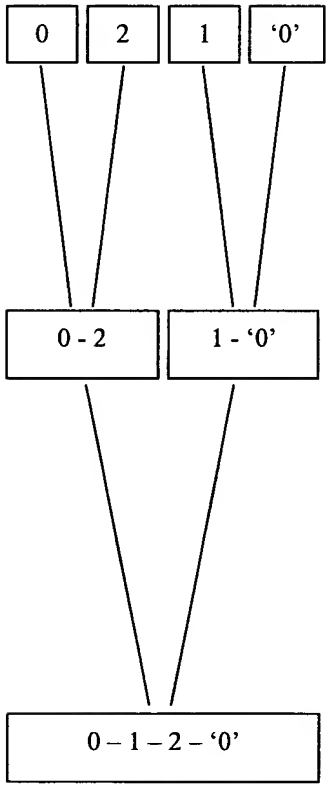

FIG. 17

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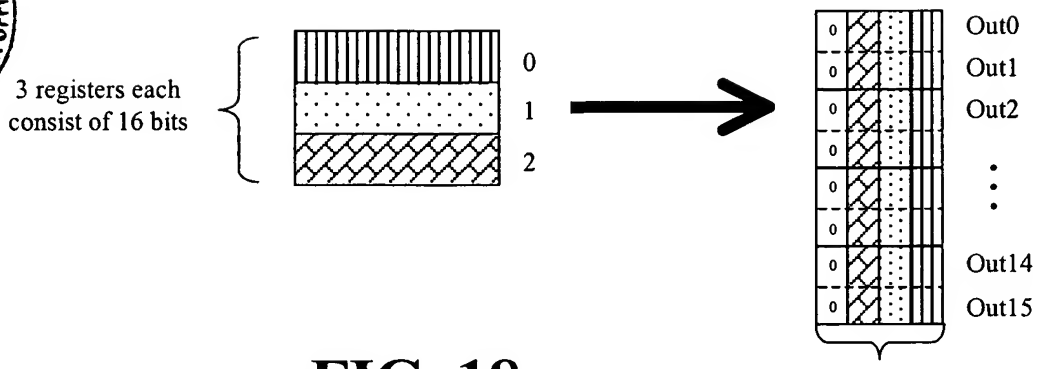

FIG. 18

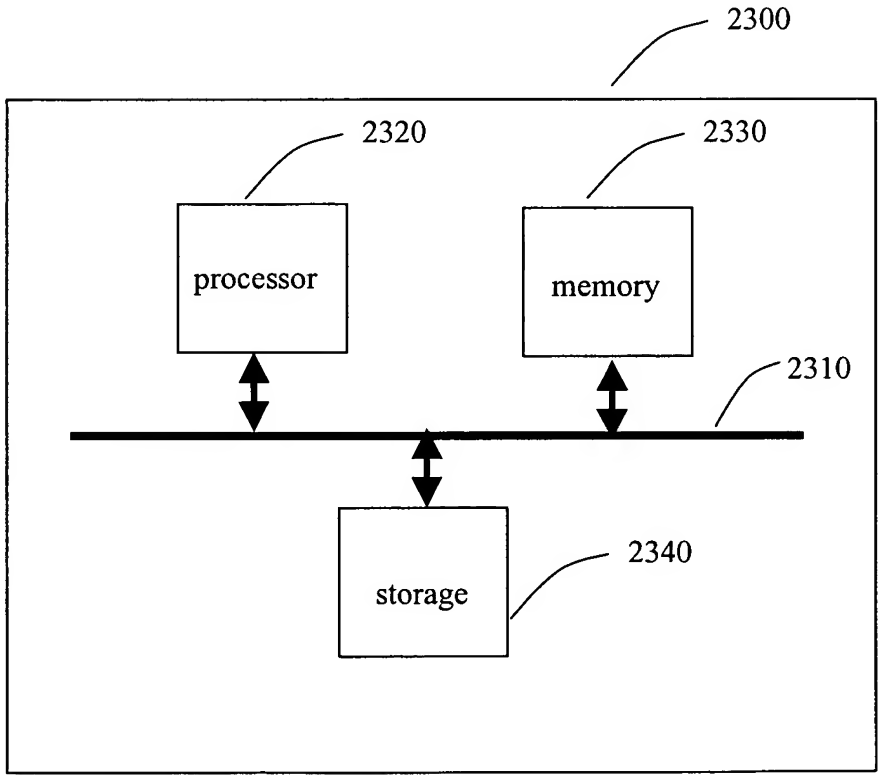

FIG. 19